

What is claimed is:

1. An image processing device for emphasizing a specified luminance area of an image taken by an image sensor, wherein any plural luminance areas of the image are selected and sensor outputs corresponding to the selected luminance areas are emphasized by using an image sensor output characteristic conversion table to increase a contrast of each image portion in each of the selected luminance areas.
2. An image processing device as defined in claim 1, wherein an image sensor output of each luminance area other than the emphasized luminance areas is fixed to zero or a specified value.
3. An image processing device as defined in claim 1, wherein a light sensor circuit representing at least one unit pixel of the image sensor has a logarithmic output characteristic.
4. An image processing device as defined in claim 3, wherein a light sensor circuit representing one unit pixel of the image sensor comprises a MOS transistor for converting a current flowing in a photo diode into a voltage signal having a logarithmic characteristic in a weak inverse state.
5. An image processing device as defined in claim 3, wherein a light sensor circuit representing one unit pixel of the image sensor comprises a MOS transistor for converting a current flowing in a photo diode into a voltage signal having a logarithmic characteristic in a weak inverse state and has a control means for discharging a remaining electric charge of a parasitic capacitor of the photo diode by preliminarily changing a drain voltage of the MOS transistor.
6. An image processing device as defined in claim 3, wherein a light sensor circuit representing one unit pixel of an image sensor has a shutter function.

7. An image processing method for emphasizing a specified luminance area of an image taken by an image sensor, comprising the steps of selecting any plural luminance areas of the image, and emphasizing the sensor outputs corresponding to the selected luminance areas by using an image sensor output characteristic conversion table to increase a contrast of each image portion in each of the selected luminance areas.

8. An image processing method as defined in claim 7, further comprising the step of setting an image sensor output of each luminance area other than the emphasized luminance areas to zero or a specified value.

9. An image processing method as defined in claim 7, wherein a light sensor circuit representing at least one unit pixel of the image sensor has a logarithmic output characteristic.

10. An image processing method as defined in claim 9, wherein a light sensor circuit representing the at least one unit pixel of the image sensor comprises a MOS transistor for converting a current flowing in a photo diode into a voltage signal having a logarithmic characteristic in a weak inverse state.

11. An image processing method as defined in claim 9, wherein a light sensor circuit representing the at least one unit pixel of the image sensor comprises a MOS transistor for converting a current flowing in a photo diode into a voltage signal having a logarithmic characteristic in a weak inverse state and has a control means for discharging a remaining electric charge of a parasitic capacitor of the photo diode by preliminarily changing a drain voltage of the MOS transistor.

12. An image processing method as defined in claim 9, wherein a light sensor circuit representing the at least one unit pixel of the image sensor has a shutter function.